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## TileSpMSpV: A Tiled Algorithm for Sparse Matrix-Sparse Vector Multiplication on GPUs

Haonan Ji ${ }^{1}$, Huimin Song ${ }^{1}$, Shibo $\mathrm{Lu}^{2}$, Zhou Jin ${ }^{1}$, Guangming Tan ${ }^{3}$ and Weifeng Liu ${ }^{1}$

1. Super Scientific Software Laboratory, China University of Petroleum-Beijing
2. Northeastern University
3. State Key Laboratory of Computer Architecture, Institute of Computing Technology, Chinese Academy of Sciences

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Introduction

## Sparse Matrix-Sparse Vector Multiplication (SpMSpV)

Sparse Matrix-Sparse Vector Multiplication (SpMSpV) operation multiplies a sparse matrix $A$ with a sparse vector $x$ and obtains a resulting sparse vector $y$.


SpMSpV

Introduction

## Row-wise SpMSpV

Each element of the resulting vector $y$ is obtained by computing the dot product of the corresponding row of matrix $A$ with the vector $x$.


Introduction

## Column-wise SpMSpV

Each nonzero in $x$ finds the corresponding column in the matrix, scales the nonzeros in the column, and merges the results into $y$.


Column-wise SpMSpV

Introduction

## BFS

BFS is one of the most basic traversal algorithms in graph computations. The algorithm starts from a source vertex in the graph and accesses all reachable vertices through multi-layer traversal.


An example of running the first iteration of BFS on the graph (left) by using SpMSpV (right).

## Introduction

## Motivation 1

- Existing work ignored exploiting local sparsity in the input sparse matrix and vector largely.

Carl Yang, Yangzihao Wang and John Owens. "Fast Sparse Matrix and Sparse Vector Multiplication Algorithm on the GPU". In IPDPSW '15, 2015, pp. 841-847.

Ariful Azad and Aydin Buluç. "A Work-Efficient Parallel Sparse Matrix Sparse Vector Multiplication Algorithm". In IPDPS '17, 2017, pp. 688-697.

Leonid Yavits and Ran Ginosar. "Accelerator for Sparse Machine Learning". In IEEE Computer Architecture Letters, 2018, pp. 21-24.

Min Li, Yulong Ao, and Chao Yang. "Adaptive SpMV/SpMSpV on GPUs for Input Vectors of Varied Sparsity". In IEEE Transactions on Parallel and Distributed Systems, 2021, pp. 1842-1853.

Paul Burkhardt. "Optimal Algebraic Breadth-First Search for Sparse Graphs". In ACM Transactions on Knowledge Discovery from Data, 2021, pp. 1-19.

Introduction

## Motivation 2

- No one matrix storage formulation works for any sparsity structure.



## Part II

## TileSpMSpV

## Storage structure of sparse matrix

TileSpMSpV divides the input sparse matrix into several sparse matrix tiles of size $n t$-by-nt, where $n t$ can be 16, 32 or 64.


Yuyao Niu, Zhengyang Lu, Meichen Dong, Zhou Jin, Weifeng Liu, and Guangming Tan. "TileSpMV: A Tiled Algorithm for Sparse Matrix-Vector Multiplication on GPUs". IPDPS '21, 2021.

## Storage structure of sparse vector

TileSpMSpV divides the input sparse vector into several sparse matrix tiles of size $n t$-by-1, then uses index and value arrays to mark the non-empty tiles information, and realizes the access of $O(1)$ time complexity.


TileSpMSpV algorithm

- Load the corresponding matrix tile into the GPU shared memory.



## TileSpMSpV algorithm

- Load the corresponding matrix tile into the GPU shared memory.
- Find the actual storage position of the corresponding vector tile called x_tile_id. If x_tile_id=-1, skip the calculation, otherwise, obtain the vector tile information.



## TileSpMSpV algorithm

- Load the corresponding matrix tile into the GPU shared memory.
- Find the actual storage position of the corresponding vector tile called x_tile_id. If x_tile_id=-1, skip the calculation, otherwise, obtain the vector tile information.
- Different kernel calculations are selected according to different formats of matrix tiles.


TileSpMSpV: vector tile is not empty



TileSpMSpV: vector tile is empty


## Part III

## TileBFS

## Auxiliary data structure for TileBFS



The non-empty tiles use a binary bitmask to record whether the elements in a tile are zero.

## Auxiliary data structure for TileBFS



The non-empty tiles use a binary bitmask to record whether the elements in a tile are zero.

> Adaptive selection of tile size $(n t=16,32$, or 64$)$.

## 16: unsigned char

32: unsigned int 64: unsigned long long

## Auxiliary data structure for TileBFS



The non-empty tiles use a binary bitmask to record whether the elements in a tile are zero.

Adaptive selection of tile size ( $n t=16,32$, or 64 ).

In tile format of row-wise and column-wise SpMSpV.

TileBFS
Auxiliary data structure for TileBFS
whether the vertex has
been visited.
The non-empty tiles use a binary bitmask to record whether the elements in a tile are zero.

Adaptive selection of tile size ( $n t=16,32$, or 64 ).

In tile format of row-wise and column-wise SpMSpV.

Vectors are stored as dense tiled vectors.

## Direction optimization of BFS



- A single operation method is difficult to maintain high performance when dealing with vectors with different sparsity.
- TileBFS designs three types of SpMSpV methods: Push-CSC, Push-CSR and Pull-CSC.


## Direction optimization of BFS——Push-CSC (First layer)



Pull-CSC method


According to the non-empty element position of the input vector, several corresponding matrix columns are found, and then the corresponding matrix columns are merged into the result vector.

When the sparsity of the $x$ is less than 0.01 and the number of unvisited vertices is large, we will
---suse Push-CSC.

## Direction optimization of BFS——Push-CSR (Second layer)



By multiplying each row tile of the matrix by the corresponding vector tile, the resulting vector tile is obtained.

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When the sparsity of the input vector $x$ is greater than or equal to 0.01 and the number of unvisited vertices is large, we will

## Direction optimization of BFS——Pull-CSC (Third layer)



- Calculate the input vector.
- Find the corresponding matrix columns.
- AND the selected matrix columns with the mask vector.
- Update mask vector according to AND result.



## Part IV

## Performance Evaluation

## Experimental setup

|  | Algorithm | Machine specification |
| :---: | :---: | :---: |
| SpMSpV | (1) TileSpMV [40] | (1) NVIDIA Geforce RTX 3060 (Ampere), <br> 3,584 CUDA cores @ 1.78 GHz , <br> 12 GB GDDR6, B/W $360.0 \mathrm{~GB} / \mathrm{s}$, <br> (2) NVIDIA Geforce RTX 3090 (Ampere), 10,496 CUDA cores @ 1.70 GHz , 24 GB GDDR6X, B/W 936.2 GB/s. |
|  | (2) cuSPARSE v11.4 BSR |  |
|  | (3) CombBLAS [3] |  |
|  | (4) TileSpMSpV (this work) |  |
| BFS | (1) Gunrock [48] |  |
|  | (2) GSwitch [37] |  |
|  | (3) TileBFS (this work) |  |

[^0] Multiplication on GPUs". IPDPS '21, 2021.

Ariful Azad and Aydin Buluç. "A Work-Efficient Parallel Sparse Matrix Sparse Vector Multiplication Algorithm". In IPDPS '17, 2017.

Yangzihao Wang, Andrew Davidson, Yuechao Pan, Yuduo Wu, Andy Riffel, and John D. Owens. "Gunrock: A High-Performance Graph Processing Library on the GPU". In PPoPP '16, 2016.

Ke Meng, Jiajia Li, Guangming Tan, and Ninghui Sun. "A Pattern Based Algorithmic Autotuner for Graph Processing on GPUs." In PPoPP '19, 2019.

## Information of the representative matrices

| Matrix | Size | \#nonzeros | \#tiles $\left(16^{*} 16\right)$ | \#tiles $\left(32^{*} 32\right)$ | \#tiles $\left(64^{*} 64\right)$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| af_5_k101 | $503 \mathrm{~K} \times 503 \mathrm{~K}$ | 17 M | 257 K | 110 K | 55 K |
| cant | $62 \mathrm{~K} \times 62 \mathrm{~K}$ | 4 M | 62 K | 20 K | 8 K |
| cavity23 | $4 \mathrm{~K} \times 4 \mathrm{~K}$ | 144 K | 2 K | 1 K | 1 K |
| pdb1HYS | $36 \mathrm{~K} \times 36 \mathrm{~K}$ | 4 M | 50 K | 19 K | 8 K |
| fullb | $199 \mathrm{~K} \times 199 \mathrm{~K}$ | 11 M | 31 K | 112 K | 220 K |
| Idoor | $952 \mathrm{~K} \times 952 \mathrm{~K}$ | 46 M | 998 K | 574 K | 380 K |
| in-2004 | $1 \mathrm{M} \times 1 \mathrm{M}$ | 27 M | 1 M | 641 K | 363 K |
| msdoor | $415 \mathrm{~K} \times 415 \mathrm{~K}$ | 20 M | 484 K | 288 K | 191 K |
| roadNet-TX | $1 \mathrm{M} \times 1 \mathrm{M}$ | 3 M | 1 M | 740 K | 464 K |
| ML_Geer | $1 \mathrm{M} \times 1 \mathrm{M}$ | 110 M | 1 M | 694 K | 332 K |
| $333 S P$ | $3 \mathrm{M} \times 3 \mathrm{M}$ | 22 M | 8 M | 7 M | 7 M |
| dielFilterV2clx | $607 \mathrm{~K} \times 607 \mathrm{~K}$ | 25 M | 2 M | 1 M | 481 K |

## Dataset

The dataset of SpMSpV contains all 2757 sparse matrices from the SuiteSparse Matrix Collection. Inside the dataset, 2081 sparse matrices are square and are used for testing BFS.

## SpMSpV performance comparison of four methods with different sparsity


the sum of matrix column elements corresponding to the positions of all nonzero elements in the vector (log10 scale)
TileSpMSpV achieves speedups of on average $1.83 x$ (up to $7.68 x$ ) over TileSpMV, 17.18 (up to 1050.02x) over cuSPARSE and 17.20x (up to 235.90x) over CombBLAS at vector sparsity of 0.1, $0.01,0.001$ and 0.0001 on RTX 3090.

## BFS performance comparison



On RTX 3060, the average speedups over Gunrock and GSwitch are $3.03 x$ and $4.35 x$, the best speedups are 21.70x and $837.36 x$, respectively.
On RTX 3090, the average speedups over Gunrock and GSwitch are 2.74x and 20.01x, the best speedups are $4.69 x$ and $1164.35 x$, respectively.

Performance Evaluation
Comparison over Gunrock and GSwitch


Performance comparison of 12 representative matrices on RTX 3090 GPU.

## Directional optimization analysis



Comparison of BFS performance using three direction optimization step by step of the representative matrices. The performance improvement of kernel conversion is significant.

## Iteration time analysis



On RTX 3090, the iteration time comparison of Gunrock, GSwitch and TileBFS.

- Suppress peak execution time(see the matrices 'in-2004' and 'msdoor')
- Invalid kernel switch(see the matrices 'msdoor' and 'cant')


## Format conversion overhead



Comparison of preprocessing time and a BFS time of the all matrices on RTX 3090. The time for format conversion does not exceed a single BFS processing time in most cases.

Comparison over Enterprise


Performance comparison of 6 representative matrices on RTX 3090 GPU. The average speedup is $1.39 x$, and the maximum speedup is 2.31 x .

## Conclusion

We develop tiled storage structures for the sparse matrix and vectors involved in SpMSpV.

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We design a tiled sparse algorithm called TileSpMSpV and a directional optimization BFS algorithm called TileBFS.

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We design a tiled sparse algorithm called TileSpMSpV and a directional optimization BFS algorithm called TileBFS.

We evaluate our algorithms on latest NVIDIA GPU and significantly outperform existing work.

## ICPP 2022

## Thanks for your time!

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